CSE113: Parallel Programming

• Topics:

- Intro to mutual exclusion
 - Different types of parallelism
 - Data conflicts
 - Protecting shared data

	g						
	0	mutex request					
		mutex acquire					
		account += 1					
	mutex request	mutex release					
	mutex acquire						
	account -= 1						
	mutex release						
↓							
time							

Announcements

- Homework due on Oct 15
 - You have everything you need to get it done
 - Three free late days, nothing accepted after that
 - Plenty of office hours remaining to get help
 - Work on your design doc before asking for help
 - We do not answer questions on the weekend
- Starting on Module 2 today!

Announcements

- Homework 1 notes:
 - No assigned speedup required. You should get a noticeable speedup from ILP
 - You can start to share results on your personal machines. Everyone's results will be slightly different
 - Sometimes you cannot account for small differences
 - Run your code for more iterations and take an average

How many elements of type double can be stored in a cache line?

□ 1			
2			
4			
8			
□ 16			
32			

Instructions with the following property should be placed as far apart as possible in machine code:

Instructions that compute floating point values

Instructions that load from memory

□ Instructions that depend on each other

□ Instructions that perform the same operation

What does ILP stand for?

Interleaved Language Program

Instruction Level Parallelism

□ Interpreted Latency Pipeline

C++ threads are initialized with a function argument where they will start execution, but they must be explicitly started with the "launch" command.

True
False

The "join" function for a C++ thread causes the thread to immediately exit.

○ True

⊖ False

A thread that is launched will eventually exit by itself and there is no need for the main thread to keep track of the threads it launches.

⊖ True

⊖ False

In 2 or 3 sentences, explain the difference between instruction level parallelism and thread parallelism

Review

Caches



Caches

int increment(int *a) {
 a[0]++;
}

%5 = load i32, i32* %4
%6 = add nsw i32 %5, 1
store i32 %6, i32* %4

4 cycles 1 cycles 4 cycles

9 cycles!



Cache Coherence and False Sharing



when one core modifies a value in the cache line, it invalidates everyone else's cache line.

This is called *False Sharing*





```
#include <thread>
#include <iostream>
using namespace std;
void foo(int a, int b, int *c) {
  // return a + b;
  *c = a + b;
int main() {
  // some main code
  int ret = 0;
  thread thread handle (foo, 1, 2, &ret);
  // code here runs concurrently with foo
  cout << ret << endl;</pre>
  thread handle.join();
  return 0;
```

What if....

SPMD programming model

- Same program, multiple data
- Main idea: many threads execute the same function, but they operate on different data.
- How do they get different data?
 - each thread can access their own thread id, a contiguous integer starting at 0 up to the number of threads

SPMD programming model

iterations computed by thread 1



switch to thread 1

Assume 2 threads lets step through thread 1 i.e. tid = 1 num_threads = 2

SPMD programming model

void increment_array(int *a, int a_size, int tid, int num_threads);

```
#define THREADS 8
#define A SIZE 1024
int main() {
  int *a = new int[A SIZE];
 // initialize a
 thread thread ar[THREADS];
  for (int i = 0; i < THREADS; i++)
   thread ar[i] = thread(increment array, a, A SIZE, i, THREADS);
  for (int i = 0; i < THREADS; i++)
   thread ar[i].join();
 delete[] a;
  return 0;
```

New material

- Abstract tasks:
 - In the abstract: a sequence of computation
 - Given an input, produces an output

- Abstract tasks:
 - In the abstract: a sequence of computation
 - Given an input, produces an output
- Concrete tasks:
 - Application (e.g. Spotify and Chrome)
 - Function
 - Loop iterations
 - Individual instructions







Sequential execution Not concurrent or parallel



C0



The OS can preempt a thread (remove it from the hardware resource)

time

Task 0 Task 1



The OS can preempt a thread (remove it from the hardware resource)





The OS can preempt a thread (remove it from the hardware resource)

tasks are interleaved on the same processor



- Definition:
 - 2 tasks are concurrent if there is a point in the execution where both tasks have started and neither has ended.



The OS can preempt a thread (remove it from the hardware resource)



- Definition:
 - 2 tasks are concurrent if there is a point in the execution where both tasks have started and neither has ended.



The OS can preempt a thread (remove it from the hardware resource)











• 2 tasks are **concurrent** if there is a point in the execution where both tasks have started and neither has ended.




- Definition:
 - An execution is **parallel** if there is a point in the execution where computation is happening simultaneously



- Examples:
 - Neither concurrent or parallel (sequential)



- Examples:
 - Concurrent but not parallel



- Examples:
 - Parallel and Concurrent



- Examples:
 - Parallel but not concurrent?



- Examples:
 - Parallel but not concurrent?



- Examples:
 - Parallel execution but task 0 and task 1 are not concurrent?



- In practice:
 - Terms are often used interchangeably.
 - *Parallel programming* is often used by high performance engineers when discussing using parallelism to accelerate things
 - *Concurrent programming* is used more by interactive applications, e.g. event driven interfaces.

Embarrassingly parallel

From Wikipedia, the free encyclopedia

In parallel computing, an **embarrassingly parallel** workload or problem (also called **embarrassingly parallelizable**, **perfectly parallel**, **delightfully parallel** or **pleasingly parallel**) is one where little or no effort is needed to separate the problem into a number of parallel tasks.^[1] This is often the case where there is little or no dependency or need for communication between those parallel tasks, or for results between them.^[2]

For this class: A multithreaded program is *embarrassingly parallel* if there are no *data- conflicts.*

A *data conflict* is where one thread writes to a memory location that another thread reads or writes to concurrently and without sufficient *synchronization*.

• Consider the following program:

There are 3 arrays: a, b, c.
We want to compute c[i] = a[i] + b[i]











- The different parallelization strategies will probably have different performance behaviors.
- But they are both embarrassingly parallel solutions to the problem
- There is lots of research into making these types of programs go fast!
 but this module will focus on programs that require synchronization

• Next Program

There are 3 arrays: a, b, c.
We want to compute c[i] = a[0] + b[i]







• Next Program

There are 2 arrays: b, c We want to compute $c[0] = b[0] + b[1] + b[2] \dots$







Conflict because multiple threads write to the same location!

Note: Reductions have some parallelism in them, as seen in your homework.



Conflict because multiple threads write to the same location!

• Many applications are not embarrassingly parallel

• Bank



My account: \$\$

• Bank



• Bank







• Graph algorithms



Examples: Ranking pages on the internet information spread in social media

• Graph algorithms

Examples:



• Graph algorithms

Examples:



• Graph algorithms



Examples: Ranking pages on the internet information spread in social media
We need a way how to safely share memory

• Machine Learning



Lots of machine learning is some form of matrix multiplication

image from: https://www.mathsisfun.com/

We need a way how to safely share memory

• Machine Learning



Lots of machine learning is some form of matrix multiplication

image from: https://www.mathsisfun.com/

We need a way how to safely share resources

• User interfaces

Run Tests

Ran 2 tests out of 366 Local iterations: 89 Killed Tests: 0 Time (seconds): 0.000000

Cance

Clear Test Log

Save to File

Action Log: using device Apple A12 GPU

Test Log: Running Test: round_robin3t_4i_99 Finished killed: 0 Success: 100 Running Test: round_robin3t_4i_95 Finished killed: 0 Success: 100 Running Test: round_robin3t_4i_94 background process that provides progress updates to the UI.

UI updates must be synchronized!!

https://drive.google.com/file/d/1JVQTQsrKhpksgVAM1yaMQky ohfDtWsSI/view?usp=sharing

Dangers of conflicts

• We will illustrate using a running bank account example

Sequential bank scenario

- UCSC deposits \$1 in my bank account after every hour I work.
- I buy a cup of coffee (\$1) after each hour I work.
- I work 1M hours (which is actually true).
- I should break even
- C++ code

Concurrent bank scenario

- UCSC contracts me to work 1M hours.
- My bank is so impressed with my contract that they give me a credit card. i.e. I can overdraw as long as I pay it back.
- UCSC deposits \$1 in my bank account at some point for every hour I work.
- I budget \$1M to spend on coffee **at some point** during work.

Concurrent bank scenario

This sets up a scheme where I buy coffee concurrently with working



Code demo

Reasoning about concurrency

- What is going on?
- We need to be able to reason more rigorously about concurrent programs

Tyler's coffee addiction:

```
for (int i = 0; i < HOURS; i++) {
    tylers_account -= 1;
}</pre>
```

Tyler's employer

```
for (int j = 0; j < HOURS; j++) {
   tylers_account += 1;
}</pre>
```

Tyler's coffee addiction:

```
for (int i = 0; i < HOURS; i++) {
    tylers_account -= 1;
}</pre>
```



```
for (int j = 0; j < HOURS; j++) {
   tylers_account += 1;
}</pre>
```

The execution of a program gives rise to events Important distinction between program and events

Tyler's coffee addiction: for (int i = 0; i < HOURS; i++) { tylers account -= 1; } i = 0 check(i < HOURS) tylers_account -= 1 time i++ (i == 1) check(i < HOURS) tylers_account -= 1 i++ (i == 2) check(i < HOURS) tylers_account -= 1

Tyler's employer

```
for (int j = 0; j < HOURS; j++) {
   tylers_account += 1;
}</pre>
```









consider just one loop iteration



Concurrent execution





one possible execution

Concurrent execution







one possible execution

Concurrent execution



tyler_account: 0

tyler_account: -1

tyler_account: 0





Another possible execution

Concurrent execution







Another possible execution

Concurrent execution





Concurrent execution

time



time

Another possible execution

This time my account isn't ever negative

 $i = 0 \quad check(i < HOURS) \quad j = 0 \quad check(j < HOURS) \quad tylers_account += 1 \quad j++ (j == 1) \quad check(j < HOURS) \quad tylers_account -= 1 \quad i++ (i == 1) \quad check(i < HOURS) \quad tyler_account: 0 \quad tyler_account: 0$



How many possible interleavings? Combinatorics question:

if Thread 0 has N events if Thread 1 has M events

 $\frac{(N+M)!}{N!M!}$

j = 0
check(j < HOURS)
tylers_account += 1
j++ (j == 1)
check(j < HOURS)</pre>

time

Concurrent execution

time

in our example there are 252 possible interleavings!



Reasoning about concurrency

- Not feasible to think about all interleavings!
 - Lots of interesting research in pruning, testing interleavings
 - Very difficult to debug
- Think about smaller instances of the problem
- **Reduce the problem**: *If there's a problem we should be able to see it in a single loop iteration.*



Lets get to the bottom of our money troubles:

For any interleaving, both of the increase and decrease must happen in some order. So there isn't an interleaving that will explain the issue.

concurrent execution



concurrent execution

tylers_account -= 1

time

tylers_account += 1

Remember 3 address code...

concurrent execution



this line of code needs to be expanded

Remember 3 address code...

concurrent execution

tylers	account	+=	1
<u> </u>			

time

time



Remember 3 address code...

concurrent execution



<pre>T1_load = *tylers_account</pre>
T1_load+= 1
<pre>*tylers_account = T1_load</pre>

What if we interleave these instructions?

concurrent execution

time



<pre>T1_load = *tylers_account</pre>
T1_load+= 1
<pre>*tylers_account = T1_load</pre>

concurrent execution

T0_load = *tylers_account

T1_load = *tylers_account

T0 load -= 1 T1_load+= 1

*tylers_account = T1_load

*tylers_account = T0_load



tylers_account has -1 at the end of this interleaving!

concurrent execution

T0_load = *tylers_account

T1_load = *tylers_account

T0 load -= 1 T1 load+= 1 *tylers_account = T1_load

*tylers_account = T0_load

What now?

- Data conflicts lead to many different types of issues, not just strange interleavings.
 - Data tearing
 - Instruction reorderings
 - Compiler optimizations
- Rather than reasoning about data conflicts, we will protect against them using *synchronization*.

Synchronization

- A scheme where several actors agree on how to safely share a resource during concurrent access.
- Must define what "safely" means.
- Example:
 - Two neighbors sharing a yard between a dog and cat
 - Sharing refrigerator with roommates
 - An account balance that is written to and read from
 - More described in Chapter 1 in text book

Mutexes

• A synchronization object to protect against data conflicts

Simple API:

lock()
unlock()

- Before a thread accesses the shared memory, it should call lock()
- When a thread is finished accessing the shared data, it should call unlock ()

Tyler's coffee addiction:

tylers_account -= 1;

Tyler's employer

tylers_account += 1;

assume a global mutex object m protect the account access with the mutex
Tyler's coffee addiction:

m.lock();
tylers_account -= 1;
m.unlock();

Tyler's employer

m.lock();
tylers_account += 1;
m.unlock();

assume a global mutex object m protect the account access with the mutex

Tyler's coffee addiction:

```
m.lock();
tylers_account -= 1;
m.unlock();
```

Tyler's employer

m.lock();
tylers_account += 1;
m.unlock();

Tyler's coffee addiction:

m.lock();

```
tylers_account -= 1;
m.unlock();
```

mutex request
mutex acquire

time

Tyler's employer



Tyler's employer

Tyler's coffee addiction:

```
m.lock();
tylers_account -= 1;
m.unlock();
```

	mutex request
time	mutex acquire
	tylers_account -= 1
	mutex release

Tyler's employer

Tyler's coffee addiction:

```
m.lock();
tylers_account -= 1;
m.unlock();
```

	mutex request
	mutex acquire
time	tylers_account -= 1
	mutex release

Tyler's employer





concurrent execution



concurrent execution

mutex request



at this point, thread 0 holds the mutex. another thread cannot acquire the mutex until thread 0 releases the mutex also called the **critical section.**

concurrent execution

mutex request mutex acquire



Allowed to request

concurrent execution

mutex acquire

mutex request

mutex request





concurrent execution

mutex acquire

mutex request

mutex request

mutex acquire

disallowed!



Thread 0 has released the mutex

concurrent execution

mutex request

mutex acquire mutex request

t tylers_account -= 1

L mutex release



Thread 1 can take the mutex and enter the critical section

concurrent execution

mutex request

mutex acquire mutex request

tylers_account -= 1 mute

mutex release mutex acquire



A mutex restricts the number of allowed interleavings Critical section are mutually exclusive: i.e. they cannot interleave

Thread 1 can take the mutex and enter the critical section

concurrent execution

mutex request

mutex acquire mutex request

tylers_account -= 1

mutex release mutex acquire

tylers_account += 1

mutex release



It means we don't have to think about 3 address code

Thread 1 can take the mutex and enter the critical section

concurrent execution

mutex request mutex acquire

e mutex request t

tylers_account -= 1 mutex

mutex release mutex acquire

tylers_account += 1

mutex release

Make sure to unlock your mutex!



time	mutex request	mutex request
	mutex acquire	mutex acquire
	tylers_account -= 1	tylers_account += 1
	<pre>printf("warning!\n");</pre>	mutex release
	\downarrow	

concurrent execution

Thread 1 is stuck!

time

mutex request

mutex acquire

tylers_account -= 1 mutex request

printf("warning!\n")

- What about timing?
 - Overhead of acquiring/releasing mutex
 - Cache flushing (heavier weight than coherence)
 - Reduces parallelism

- What about timing?
 - Overhead of acquiring/releasing mutex
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in a parallel system without the mutex



- What about timing?
 - Overhead of acquiring/releasing mutex
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in a parallel system with the mutex



Long periods of waiting in the threads

Try to keep mutual exclusion sections small!

Code example with overhead

Try to keep mutual exclusion sections small! Protect only data conflicts!

Code example with overhead



Long periods of waiting in the threads

Try to keep mutual exclusion sections small! Protect only data conflicts!

Code example with overhead



overlap the overhead (i.e. computation without any data conflicts)

Mutex alternatives?

Other ways to implement accounts?

Atomic Read-modify-write (RMWs): primitive instructions that implement a read event, modify event, and write event indivisibly, i.e. it cannot be interleaved.

```
atomic_fetch_add(atomic_int * addr, int value) {
    int tmp = *addr; // read
    tmp += value; // modify
    *addr = tmp; // write
}
```

other operations: max, min, etc.

Tyler's coffee addiction:

```
m.lock();
tylers_account -= 1;
m.unlock();
```

Tyler's employer

```
m.lock();
tylers_account += 1;
m.unlock();
```

time

```
Tyler's coffee addiction:
```

```
m.lock();
tylers_account -= 1;
m.unlock();
```

Tyler's employer

m.lock();
tylers_account += 1;
m.unlock();

time

Tyler's coffee addiction:

Tyler's employer

tylers_account -= 1;

tylers_account += 1;

time

Tyler's coffee addiction:

atomic_fetch_add(&tylers_account, -1);

Tyler's employer

atomic_fetch_add(&tylers_account, 1);

time

Tyler's coffee addiction:

atomic_fetch_add(&tylers_account, -1);

Tyler's employer

atomic_fetch_add(&tylers_account, 1);

atomic fetch add(&tylers account, -1);

time

atomic_fetch_add(&tylers_account, 1);

Tyler's coffee addiction:

atomic_fetch_add(&tylers_account, -1);

Tyler's employer

atomic_fetch_add(&tylers_account, 1);

atomic fetch add(&tylers account, -1);

time

time

atomic_fetch_add(&tylers_account, 1);

Two indivisible events. Either the coffee or the employer comes first either way, account is 0 afterwards.

Tyler's coffee addiction:

atomic_fetch_add(&tylers_account, -1);

Tyler's employer

atomic_fetch_add(&tylers_account, 1);

atomic fetch add(&tylers account, -1);

time

atomic_fetch_add(&tylers_account, 1);

Code example

Atomic RMWs

Pros? Cons?

Atomic RMWs

Pros? Cons?

Not all architectures support RMWs (although more common with C++11)

Limits critical section (what if account needs additional updating?)

Multiple mutexes

Lets say I have two accounts:

- Business account
- Personal account
- Need to protect both of them using a mutex
 - Easy, we can just the same mutex

Multiple mutexes

Lets say I have two accounts:

- Business account
- Personal account
- No reason individual accounts can't be accessed in parallel
Lets say I have two accounts:

- Business account
- Personal account
- No reason individual accounts can't be accessed in parallel



Long periods of waiting in the threads

Mutexes are objects. We can create multiple versions of them to protect different shared data.

MutexP for personal account MutexB for business account



Mutexes are objects. We can create multiple versions of them to protect different shared data.

MutexP for personal account MutexB for business account



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Mutexes are objects. We can create multiple versions of them to protect different shared data.

MutexP for personal account MutexB for business account



Managing multiple mutexes

Consider this increasingly elaborate scheme

My accounts start being audited by two agents:

- UCSC
- IRS
- They need to examine the accounts at the same time. They need to acquire both locks

Managing multiple mutexes

Consider this increasingly elaborate scheme My accounts start being audited by two agents:

- UCSC
- IRS

```
void irs_audit() {
  for (int i = 0; i < NUM_AUDITS; i++) {
    tylers_personal_account_mutex.lock();
    tylers_business_account_mutex.lock();</pre>
```

AUDIT(tylers_personal_account, tylers_business_account);

```
tylers_personal_account_mutex.unlock();
tylers_business_account_mutex.unlock();
```

```
void ucsc_audit() {
  for (int i = 0; i < NUM_AUDITS; i++) {
    tylers_business_account_mutex.lock();
    tylers_personal_account_mutex.lock();
    AUDIT(tylers_personal_account, tylers_business_account);
    tylers_personal_account_mutex.unlock();
    tylers_business_account_mutex.unlock();
  }
}</pre>
```

• Our program deadlocked! What happened?



UCSC

mutexP request











• Our program deadlocked! What happened?

IRS has the personal mutex and won't release it until it acquires the business mutex. UCSC has the business mutex and won't release it until it acquires the personal mutex.

This is called a deadlock!



- Our program deadlocked! What happened?
- Fix: Acquire mutexes in the same order
- Proof sketch by contradiction
 - Thread 0 is holding mutex X waiting for mutex Y
 - Thread 1 is holding mutex Y waiting for mutex X

Assume the order that you acquire mutexes is X then Y Thread 0 cannot hold mutex Y without holding mutex X. Thread 1 cannot hold mutex X because thread 0 is holding mutex X Thus the deadlock cannot occur

- Our program deadlocked! What happened?
- Fix: Acquire mutexes in the same order

Double check with testing

- Proof sketch by contradiction
 - Thread 0 is holding mutex X waiting for mutex Y
 - Thread 1 is holding mutex Y waiting for mutex X

Assume the order that you acquire mutexes is X then Y Thread 1 cannot hold mutex Y without holding mutex X. Thread 1 cannot hold mutex X because thread 0 is holding mutex X Thus the deadlock cannot occur

Programming with mutexes can be HARD!

make sure all data conflicts are protected with a mutex

keep critical sections small

balance between having many mutexes (provides performance) but gives the potential for deadlocks

Thanks!

- Next time:
 - Implementing Mutexes